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Sub	stitute for form 1449/PTO		Complete if Known			
				Application Number	10/598,775-Conf. #1128	
IN	NFORMATI	ORMATION DISCLOSURE	CLOSURE	Filing Date	September 11, 2006	
STATEMENT BY APPLICANT			PPLICANT	First Named Inventor	Jorg Gliese	
				Art Unit	N/A	
	(Use as man	y sheets as i	necessary) ———	Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	V0195.0093	

U.S. PATENT DOCUMENTS						
Examiner	Cite No.1	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
Initials*		Number-Kind Code <sup>2</sup> ( if known)				
AT	AA*	US-5,592,107	01-07-1997	McDermott et al.		
	AB*	US-6,130,553	10-10-2000	Nakaya		
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	No.1	Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				78
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"EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. "CITE NO.: Those application(s) which are marked with an single asterisk (\*) next to the Cite No. are not supplied (under 37 CFR 1.98(a)(2)(iii)) because that application was filed after June 30, 2003 or is available in the IFW. Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at <a href="https://www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the magazine, journal, senal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publis and/or country where published.		T2		
AT	CA	Kim,H. et al.; "Design of heuristic algorithms based on Shannon expansion for low-power logic circuit synthesis"; IEE ProcCircuits Devices Syst., Vol. 144, No. 6, December 1997, pages 355-360.			
	СВ	Wannemacher, M; "Das FPGA-Kochbuch"; ("The FPGA Cookbook"); figure 6.4: SRAM cell from XILINX, 1st edition, International Thomson Publishing Company, Bonn, 1998, page 111.	V		
V	CC	Wannemacher, M; "Das FPGA-Kochbuch" ("The FPGA Cookbook); figure 7.36; logic block (CLB) of the XC4000 families, 1st edition, International Thomson Publishing Company, Bonn, 1998, page 197.  1998, page 197.	V		

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